



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:	Colin Stephen Gormley	GROUP:	2811
SERIAL NO:	09/661,766	EXAMINER:	Quang D. Vu
FILED:	9/14/00	Date of mailing of Notice of Allowance:	5/19/04
FOR:	METHOD FOR FORMING A SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR DEVICE FORMED BY THE METHOD		

**Mail Stop Issue Fee**  
**Assistant Commissioner of Patents**  
**P.O. Box 1450, Alexandria, VA 22313-1450**

**Attn: Official Draftsman**

**TRANSMITTAL OF FORMAL DRAWINGS**

Applicant submits herewith new drawing(s) for this application. Attached please find three (3) sheets of formal drawings for this application.

The three month period of response set in the Notice of Allowability (PTOL 37) expires on August 19, 2004, and this submission is on or before this expiry date.

Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.

Deborah M. Costello

Date: 7/28/04